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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

4

Application Number

10/655,363

Filing Date

09/04/2003

First Named Inventor

John G. Edalen

Art Unit

2861

Examiner Name

Nguyen, Lamson D.

Attorney Docket Number

2001-0886.01

ENCLOSURES

(Check all that apply)



Fee Transmittal Form



Fee Attached



Amendment/Reply



After Final



Affidavits/declaration(s)



Extension of Time Request



Express Abandonment Request



Information Disclosure Statement



Certified Copy of Priority Document(s)

Reply to Missing Parts/
Incomplete ApplicationReply to Missing Parts
under 37 CFR 1.52 or 1.53

Drawing(s)



Licensing-related Papers



Petition

Petition to Convert to a
Provisional ApplicationPower of Attorney, Revocation
Change of Correspondence Address

Terminal Disclaimer



Request for Refund



CD, Number of CD(s) _____

☐ Landscape Table on CD

After Allowance Communication to TC

Appeal Communication to Board
of Appeals and InterferencesAppeal Communication to TC
(Appeal Notice, Brief, Reply Brief)

Proprietary Information



Status Letter

Other Enclosure(s) (please identify
below):

Issue Fee Transmittal

Comments on Statement of Reasons for
Allowance

Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name

Lexmark International, Inc.

Signature

Printed name

Scott N. Barker

Date

9/22/2006

Reg. No.

42,292

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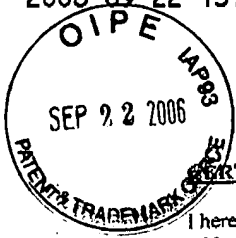
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Scott N. Barker

Practitioner's Docket No. 2001-0886.01***PATENT*****IN THE UNITED STATES PATENT & TRADEMARK OFFICE**

Applicant: John G. Edelen, et al. : Paper No. _____
Application No.: 10/655,363 : Group No.: 2861
Filed: September 4, 2003 : Examiner: Nguyen, Lamson D.

For: **N-WELL AND OTHER IMPLANTED TEMPERATURE SENSE
RESISTORS IN INKJET PRINT HEAD CHIPS**

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

Applicants respectfully submit the following remarks responsive to the Notice of Allowance dated June 27, 2006.

REMARKS

In the Notice of Allowability mailed June 27, 2006, the Examiner indicated that the "primary reasons for allowance for claims 1-31 are the inclusions of the limitations of an apparatus and a method of controlling temperature of a printhead comprising temperature sense resistors implanted in a silicon substrate of a printhead chip and comprising atoms of an implantation material, wherein the atoms of the implantation material are embedded beneath the substrate, the temperature sense resistors being

operatively connected to the controller of resistor elements to enable the controller to monitor chip temperature to control the resistor elements to heat the chip.”

In response, Applicants wish to point out that each of the claims is separately allowable over the prior art of record because of a combination of elements. In addition, it should be noted that each of claims 1-31 also stands on its own. Furthermore, all of the referenced allowed claims do not include the limitations cited by the Examiner.

For example, claim 16 recites, among other things, implanting temperature sense resistors in the substrate of a chip, wherein the operation of implanting comprises directing a beam of energetic ions incident upon the substrate to embed those ions into the substrate. Among other aspects, claim 16 does not require the substrate to be silicon or that atoms of implantation material be embedded “beneath the substrate.”

Moreover, even though the Examiner’s recitation appears to mirror language found in claim 1, there are also some differences between the recitation and the actual language of claim 1 as allowed. For example, claim 1 requires atoms of implantation material to be embedded beneath the substrate surface, not necessarily beneath the substrate itself.

Respectfully submitted,
JOHN G. EDELEN ET AL.

by 

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